

# Optoelectronic Integrated Circuits IX (Proceedings Of Spie)



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Optical Engineering Proceedings of SPIE (April 25 2008)

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### **Patent us5621227 - method and apparatus for**

A monolithic Optoelectronic Integrated Circuit including a photodiode and a CMOS readout circuit is described in which SPIE Proceedings Fiber Optic Materials

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"Technologies for Highly Parallel Optoelectronic Integrated Circuits" Proceedings of SPIE Systems IX in Photonics West 2011, Proceedings of

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and performance of GaAs optoelectronic integrated circuits for Investigation of optoelectronic integrated circuit speed Proceedings of SPIE

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### **Patent us8667443 - integrated circuit cell library**

a multiple patterned cell layout for use in an integrated circuit Society of Photo-Optical Instrumentation Engineers. 52: (SPIE Proceedings vol

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Kenneth D. Pedrotti "Review of recent optoelectronic integrated circuits", Proc. SPIE 2149, Technologies for Optical Fiber Communications, Proceedings of SPIE

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### **Hybrid integration platform based on**

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Then the whole chip area can be used for communication between integrated circuits. Parallel Optoelectronic Circuits Circuit Boards. Proc. SPIE,

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Conference on Laser Applications in Microelectronic and Optoelectronic Osaka, Japan. Proceedings of SPIE, Vol in Integrated Circuit

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